Santien

# UNIVERSITY OF CALIFORNIA, BERKELEY College of Engineering Department of Electrical Engineering and Computer Sciences

EE 105: Microelectronic Devices and Circuits

Fall 2009

## MIDTERM EXAMINATION #1

Time allotted: 45 minutes

NAME:				
STUI	DENT ID#:			
1.	SHOW YOUR WORK. (Make your methods clear to the grader!)  Specially, while using chart, make sure that you indicate how you have got your numbers. For example, if reading off mobility, clearly write down what doping density that corresponds to.  Clearly mark (underline or box) your answers.  Specify the units on answers whenever appropriate.			

SCORE:	1	/ 15
	2	/ 15
	3	/ 20
Total		/ 50

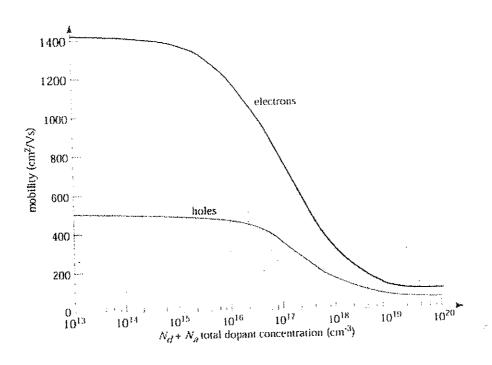
### PHYSICAL CONSTANTS

I II I SICAL COMBIANTS			PROPERTIES OF	CIT ICON	AT 300K
<u>Description</u> Electronic charge	$\frac{\text{Symbol}}{q}$	$\frac{\text{Value}}{1.6 \times 10^{-19}} \text{ C}$	<u>Description</u>	$\frac{\text{Symbol}}{E_{\text{G}}}$	<u>Value</u> 1.12 eV
Boltzmann's constant	k	8.62×10 <sup>-5</sup> eV/K	Band gap energy Intrinsic carrier	$n_{\rm i}$	10 <sup>10</sup> cm <sup>-3</sup>
Thermal voltage at 300K	$V_{\rm T} = kT/q$	0.026 V	concentration Dielectric permittivity	$arepsilon_{\mathrm{Si}}$	1.0×10 <sup>-12</sup> F/cm

#### **USEFUL NUMBERS**

 $V_T \ln(10) = 0.060 \text{ V} \text{ at } T = 300 \text{K}$ exp(30) ~  $10^{13}$ 

## Electron and Hole Mobilities in Silicon at 300K



**Prob 1**. **[15 pts]** Let us assume that we have a Si sample of length 16  $\mu$ m and a cross sectional area of 10  $\mu$ m<sup>2</sup> at room temperature. Also assume that we apply 1V across this sample.

- (a) If it is desired to have an electron density that is 10<sup>5</sup> times the intrinsic density, [4 pt]
  - (i) What kind of dopant atoms is needed? Can you give an example?
  - (ii) What doping density will be required?

(i) hence 
$$\rightarrow 41 \text{ V} \rightarrow \text{Phesphereus}$$
  
(ii)  $n = 10^{15}/\text{ cm}^3 \approx N_D$ 

(b) Estimate the drift velocity of this sample.

(c) Now let us assume that we counter-dope the sample with opposite type of dopants with a density that is 3 times more than the previous dopant density. Estimate the resistance and current flowing in this sample under these conditions. [6 pt]

$$R = f \frac{L}{A} = \frac{1}{5} - \frac{L}{A}$$

$$J := \frac{1}{16} \frac{1}{80} \cdot \frac{1}{10} \cdot \frac{1}{1$$

Prob 2[15 pts]. Consider a p-n junction diode of Si as shown below:

	**************************************
N <sub>A</sub> =10 <sup>18</sup> cm <sup>-3</sup>	$N_D = 10^{16} \text{ cm}^{-3}$
NO. 4.0.	

(a) Find out the built in potential and the depletion width at T=300K.

[4 pt]

$$V_{0} = V_{1} \ln \frac{k_{1} k_{1} k_{1}}{m_{1}^{2}}$$

$$V_{0} = V_{1} \ln \frac{k_{1} k_{1} k_{1}}{m_{1}^{2}}$$

$$V_{0} = V_{1} \ln \frac{k_{1} k_{1} k_{1}}{m_{2}^{2}}$$

$$V_{0} = V_{0} \ln \frac{k_{1} k_{1}}{m_{2}^{2}}$$

$$V_{0} = V_{0} \ln \frac{k_{1$$

(b) Assume that at T=300K a voltage of 0.9 volt is applied across the diode such that the diode is forward biased. How much should the voltage have to be changed if at T=400 K one needs to ensure that the same current is flowing as in T=300K? Assume no change in bandgap due to change in temperature.
[5 pt]

Same convent appears when:

$$V_1 - V_{i1} - V_2 V_{i2}$$

Built in Built in pot.

 $V_{62} = V_{72} I_{11} \frac{BA2B}{V_{i1}^{2}} \frac{112}{V_{i2}}$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 
 $112$ 

$$V_{02} = \frac{4}{3} \times \frac{9}{4} \times \frac{V_{01} + V_{02}}{0.06}$$

$$= 0.72$$

$$V_{0} = V_{01} + V_{02}$$

$$= 0.9 - 0.89 + 0.72$$

$$V_{02} = 0.9 + 0.72$$

$$J_{n} = \frac{e^{nx^{2}}}{L} \frac{Dn}{NA} e^{\frac{VD}{T}} \frac{4c}{2}$$

$$= \frac{(nx^{2}V_{1})}{L} \frac{\mu_{n}}{NA} e^{\frac{VD}{T}} \frac{2c}{2c} \times \frac{2c}{2c} \times \frac{3c}{2c} \times \frac{3c}$$

$$J_{P} = \frac{4n_{i}^{2}}{L} \times \frac{p_{i}p_{i}}{N_{D}} = J_{n}$$

$$\frac{N_{i}A}{N_{D}} = \frac{p_{i}p_{i}}{p_{i}}$$

#### Prob 3. [20 pts]Bipolar junction transistors.

- (a) Design Fundamentals [4 pts].
  - (i) Why is the base region doped more lightly than the emitter?

[2 pt]

(ii) Why is the width of the base region narrower than emitter and collector regions? [2 pt]

(b) Write down the condition for active mode operation and saturation mode operation for a NPN bipolar junction transistor. Consider the following transistor that has  $\beta$ =100. If it is desired that 1 mA current must be flowing in the collector at Vcc=1 V, what is the maximum value for RL that can be used before the transistor goes from active to saturation mode? [5pt]

Lagr of Sat; 
$$V_{R} = V_{C}$$
  $V_{R} = V_{C}$   $V_{R} = V_{C}$ 

(c) Draw the small signal model for the BJT descried in part (b) assuming no Early effect. Indicate the numerical values and units for all the components of this small signal model. [5 pt]

$$V_{11} = \frac{16}{V_{11}} = \frac{16}{26 \times 16} = \frac{1}{26 \times 16} = \frac{$$

(d) Early effect/ base width modulation causes the collector current to increase with collector-emitter voltage,  $V_{CE}$ . Normally it is assumed that the base current remains independent of  $V_{CE}$ . However, if the depletion region spreads a long distance into the base, it can reduce the base current by recombination of injected holes with the negative immobile charges in the base region (for a NPN transistor). This is shown in the following figure. Considering this decrease of  $I_B$  with  $V_{CE}$ , derive and draw complete (including conventional early effect and the new phenomenon) the small-signal model of a NPN transistor. [6 pt]

