# UNIVERSITY OF CALIFORNIA, BERKELEY College of Engineering Department of Electrical Engineering and Computer Sciences

## **MIDTERM EXAMINATION**

EE 130/230A: Spring 2016 Time allotted: 60 minutes

NAM	E: Solution
STUI	DENT ID#:
INST	RUCTIONS:
1.	Unless otherwise stated, assume a. temperature is 300 K b. material is Si
•	<ul> <li>SHOW YOUR WORK. (Make your methods clear to the grader!)</li> <li>Specially, while using chart, make sure that you indicate how you have got your numbers. For example, if reading off mobility, clearly write down what doping density that corresponds to.</li> <li>Clearly write down any assumption that you have made.</li> <li>Clearly mark (underline or box) your answers.</li> <li>Specify the units on answers whenever appropriate.</li> </ul>
	SCORE: 1 / 20
	2/ 20
	Total/ 40

#### PHYSICAL CONSTANTS

Description	Symbol	Value	PROPERTIES OF	SILICON	AT 300K
Electronic charge	q	1.6×10 <sup>-19</sup> C	Description	Symbol	Value
Boltzmann's constant	$\boldsymbol{k}$	8.62×10 <sup>-5</sup>	Band gap energy	$E_{\mathrm{G}}$	1.12 eV
		eV/K	Intrinsic carrier	$n_{\scriptscriptstyle 1}$	$10^{10}  \text{cm}^{-3}$
Thermal voltage at	$V_{\rm T} =$	0.026 V	concentration		
300K	kT/q		Dielectric permittivity	$\epsilon_{\mathrm{Si}}$	$1.0 \times 10^{-12}$
					F/cm

## **USEFUL NUMBERS**

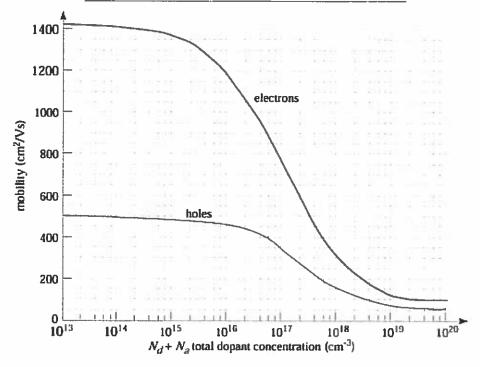
 $V_T \ln(10) = 0.060 \text{ V} \text{ at } T=300 \text{K}$ 

# Depletion region Width:

$$W = \sqrt{\frac{2\varepsilon}{q} \left(\frac{1}{N_a} + \frac{1}{N_d}\right) \left(V_{hi} - V_{Applied}\right)}$$

$$n_i = \sqrt{N_c N_v} \exp(-E_G / 2k_B T)$$

## **Electron and Hole Mobilities in Silicon at 300K**



### Prob 1 [20 pts].

- (a) [8 pt]
  - (i) What kind of dopant would one use to dope an III-V semiconductor like GaAs. What determines whether or not it is a donor or acceptor? [4 pt]
  - (ii) After doping a Si by donors it is found that the carrier concentration does not change appreciably from OK-400K. How can this happen? Explain by drawing Ec, Ev and the energy level for the dopants, Ed. [4pt]
- (i) group iv elements such as Si.

  The relative size of the dopant atom compared to the constituent atoms determine which of the two species will be replaced by the dopant and hence whether the dopant will all as a donor or acceptor.
- (ii) This can happen if the doping level is so doponts, that the energy levels for Ed is inside the conduction band. This will mean the donors can ionize without needing help from temporature.

  The doping level No must also be much higher than no at room temperature.

EC

(b) [6 pt] A compensated Si sample was measured and it was found that the conductivity is  $\sigma=16$  (ohm-cm)<sup>-1</sup> and mobility of 375 cm<sup>2</sup>/V-sec. Find out the doping concentration of the sample.

$$\frac{16}{1.6 \times 16^{-19} \times 375} = \frac{10^{20}}{375} = \frac{1}{3.75} \times 10^{18} / \text{cm}^3$$

$$= 2.67 \times 10^{17} / \text{cm}^3$$

From the  $\mu$  vs. No+ NA,  $\mu$  = 375 cm/v-sec can occur for holes at Nb+ NA =  $10^{17}/\text{cm}^3$ for electrons at Nb+ NA =  $8\times10^{17}/\text{cm}^3$ 

This means the si has to be n-type.

$$N_D + N_A = 8 \times 10^{17}$$
  
 $N_D - N_A = 2 - 67 \times 10^{17}$ 

$$N_{A} = 5.335 \times 10^{17} / \text{cm}^{3}$$

$$N_{A} = 2.67 \times 10^{17} / \text{cm}^{3}$$

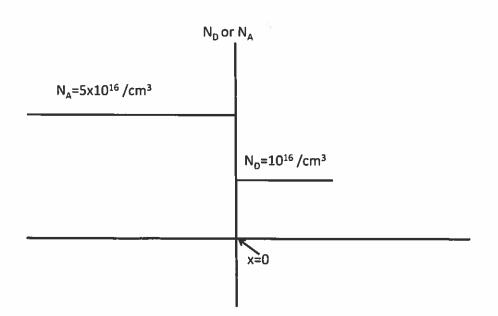
#### (c)[6 pts] Please indicate 'True' or 'False'

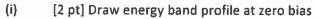
Phenomenon	True/False
Impurity scattering goes up at higher temperature	F
In a solid electrons and holes see same electrostatic potential	F
At equilibrium flows of electrons and holes balance each other to give zero current	F
In Si electron velocity saturates at high electric field due to impurities	F
When low level injection holds, $np=n_i^2$ also holds	-
Diffusion depends on the total number of carriers and not only on the excess carriers	F

**Prob 2: [20 pts]** For the P-N junction diode shown below, answer the following questions. Note that the picture shown below only shows the amplitude (and not 'sign') of the doping concentrations.

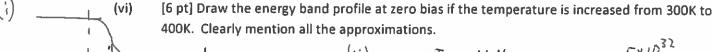
While drawing the diagrams,

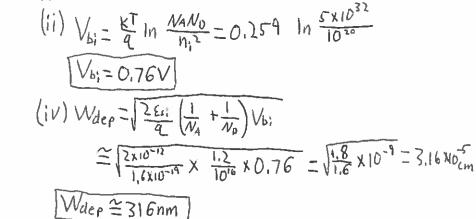
- please be careful to indicate *relative quantities* (such as the amplitude of charge densities, the curvature of potential profiles, the width of the depletion region etc)
- For each diagram you need to draw quantities at both the P and N sides
- If you are using the same diagram to answer multiple questions, please mark the answers clearly.





- (ii) [3 pt] Calculate the built in potential
- (iii) [3 pt] Draw the electric field and potential profile
- (iv) [3 pt] Calculate the depletion region width
- (v) [3 pt] Draw the energy band profile if a reverse bias of |Vd| is applied.





n.2 goes up with temperature, therefore Vbi decreases, (See Pierret Fig. 2.20)

With smaller Voi, Wdep decreases

