Solutions for Midterm #1 - EECS 145M Spring 2003

1a The transparent latch has a digital input, and digital output, and a digital control input that puts the circuit into transparent mode or hold mode. In transparent mode the output is equal to the input. When the circuit is switched to hold mode the output is held at its current value.

[5 points off if no hold mode]

1b The tri-state buffer has a digital input, and digital output, and a digital control input that puts the circuit into transparent mode or tri-state mode. In transparent mode the output is equal to the input. In tri-state mode the output is in a high-impedance state that neither drives nor loads anything connected to it.

[5 points off if no high impedance mode]

2 See Figures 3.16 and 3.17 on page 169 of the textbook

[2 points off for using an amplifier rather than a comparator]

[2 points off for not starting with all bits zero]

[5 points off for describing the tracking A/D converter]

3

- Acquire a computer with a digital I/O port and an internal clock
- Connect a pushbutton switch to one line of the digital input port and one line of the output port to an amplifier and fast indicator light (like an LED)
- Write a computer program that detects a pushbutton, waits a random delay, prompts the subject by turning on the light and measures the time until the pushbutton is pressed
- Select a large number of candidates representing group d (racecar drivers) and group p (jet fighter pilots)
- After initial training and a good nights sleep, take as much reaction time data as possible, allowing for rest periods
- Compute the average reaction times \overline{d} and \overline{p} , the standard deviation of the samples σ_d and σ_p , and the standard errors of the mean $\sigma_{\overline{d}}$ and $\sigma_{\overline{p}}$ and compute Student's t:

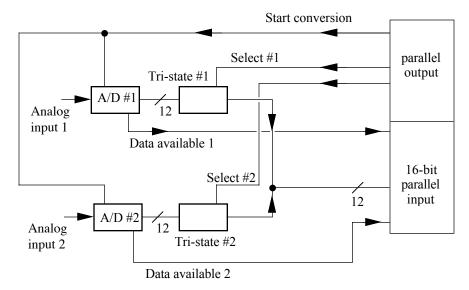
$$t = \frac{\Delta}{\sigma_{\Delta}} = \frac{\overline{d} - \overline{p}}{\sqrt{\sigma_{\overline{d}}^2 + \sigma_{\overline{p}}^2}} = \frac{\overline{d} - \overline{p}}{\sqrt{\sigma_{d}^2 / m_d + \sigma_{p}^2 / m_p}}$$

• Look up the probability of exceeding this value or t by chance. If the probability is < 0.1%, then the group with the lowest average has a faster reaction time.

[2 points off for only 10 measurements]

[5 points off if reaction time includes the random computer delay before the prompt]

[5 point off if Student's t and probability not determined]



[5 points off for not using tri-state buffers for selectively connecting the A/D outputs to the parallel input port]

4b

- Read the system timer to get the current tick count n in ms and set data storage index i = 0.
- Write word to parallel output port that has start conversion LOW, select LOW, and select LOW (assume that select = LOW is tri-state)
- Write word to parallel output port that has start conversion HIGH, select1 LOW, and select2 LOW. This causes both A/Ds to convert simultaneously]
- 4 Read parallel input port in a loop until data available 1 goes HIGH
- 5 Write word to parallel output port that has start conversion HIGH, select1 HIGH, and select2 LOW
- 6 Read the parallel port, mask off handshaking bits, and store as data1(i).
- 7 Read parallel input port in a loop until data available 2 goes HIGH
- 8 Write word to parallel output port that has start conversion HIGH, select1 LOW, and select2 HIGH
- 9 Read the parallel port, mask off handshaking bits, and store as data2(i).
- 10 Set i = i + 1 and read the system timer until the tick counter reads n + 1000 i
- 11 loop back to step 3
 - [3 points off for reading the tri-state outputs before they are enabled]
 - [3 points off for not reading the A/Ds sequentially]
 - [3 points off for not starting both A/D converters simultaneously]
 - [3 points off for not sampling the two waveforms once per second]

Midterm #1 class statistics:

Problem	max	average	rms
1	20	18.6	2.7
2	25	20.5	7.2
3	25	22.7	3.4
4	30	24.2	5.8
total	100	86.0	14.5

Grade distribution:

Range	number	approximate
		letter grade
50-54	1	F
55-59	0	D
60-64	2	C-
65-69	0	C
70-74	0	C+
75-79	1	В–
80-84	4	В
85-89	1	B+
90-94	2	A-
95-99	5	A
100	3	A+